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## **Editorial Board**

### **Faculty Members**

Ms.D.Ragavi, AP/ECE

Ms.J.Jenisha, AP/ECE

### **Students**

R.Bubalakannan, III ECE

R.Harish, III ECE

S.P.Devasri, II ECE

The Department of Electronics and Communication Engineering was established in the year 2007. The department has an intake of 60 students in B.E. course. The department possesses the most advanced equipment in its laboratories. It also provides opportunities to grow and excel in the technical world by conducting regular workshops and programs in various fields. The department is highly active in research work in the fields of broadband communications, VLSI Design, image processing etc. The students are provided adequate training in the field of signal processing, image processing and digital communication. Students are highly motivated to attend in-plant training in some of the most prestigious organizations during their time with the institution.

### **Vision of the Department:**

1. To provide the quality education in the field of Electronics and Communication Engineering which caters the needs of the society in line with the technological revolution.

### **Mission of the Department:**

1. To upgrade the technical knowledge of the students continuously by providing industrial exposure and innovative projects.
2. To establish a creative learning environment for the students by active learning of the techniques in the electronics and communication engineering field.
3. To nurture career improvement by facilitating skill development and training in the recent technologies.

## PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

**PEO1:** To provide the students with a strong foundation in the required sciences in order to pursue studies in Electronics and Communication Engineering.

**PEO2:** To gain adequate knowledge to become good professional in electronic and communication engineering associated industries, higher education and research.

**PEO3:** To develop attitude in lifelong learning, applying and adapting new ideas and technologies as their field evolves.

**PEO4:** To prepare students to critically analyze existing literature in an area of specialization and ethically develop innovative and research oriented methodologies to solve the problems identified.

**PEO5:** To inculcate in the students a professional and ethical attitude and an ability to visualize the engineering issues in a broader social context.

## PROGRAM SPECIFIC OUTCOMES (PSOs)

**PSO1:** Design, develop and analyze electronic systems through application of relevant electronics, mathematics and engineering principles

**PSO2:** Design, develop and analyze communication systems through application of fundamentals from communication principles, signal processing, and RF System Design & Electromagnetism.

**PSO3:** Adapt to emerging electronics and communication technologies and develop innovative solutions for existing and newer problems.

## VUYHAA'24

VYUHAA '24, an interdepartmental cultural fest, was held on March 9, 2024, featuring a variety of competitions such as solo dance, singing, mimicry, craftwork, face painting, group dance, fireless cooking, short film, mime, and instrumental music. The event witnessed enthusiastic participation from students, showcasing their exceptional talents.

The Department of Electronics and Communication Engineering emerged as **the overall champion**, winning the prestigious overall trophy. Prizes were distributed to the winners by our Principal and Program Coordinators, making the event a grand success.





## DEPARTMENT ACTIVITIES

The Department of Electronics and Communication Engineering organized a workshop on Digital Design with Verilog HDL and Xilinx Vivado for III-year ECE students from 16.03.2024 to 17.03.2024. The session was conducted by our esteemed alumnus, Mr. V. Govindaraj, and aimed to provide students with practical insights and hands-on experience in digital VLSI design. The workshop offered valuable exposure to industry-relevant tools, enhancing the students' understanding of hardware description languages and FPGA-based design.

**Two Days Workshop on  
Digital Design with Verilog HDL  
&  
Xilinx Vivado**

16<sup>th</sup> & 17<sup>th</sup> March 2024  
@ 09.00 am - 04.15 pm

C Block - DSP Laboratory

Exclusively for III-ECE Students

**Mr. V. GOVINDARAJ**  
(Alumnus of ChettinadTech)

Chettinad  
College of Engineering & Technology  
Approved by AICTE, New Delhi and Affiliated to Anna University, Chennai.

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## INDUSTRIAL VISIT

On March 22 and 23, 2024, the Electronics and Communication Engineering students from I, II, III, and IV years visited Kerala Electrical & Allied Engineering, Kochi, and MASCO Tea Factory, Vagamon. The visit provided students with valuable insights into industrial operations, manufacturing processes, and the technologies implemented in electrical and allied engineering industries. This hands-on exposure helped bridge the gap between theoretical knowledge and practical applications, enhancing their understanding of industry standards and practices.





## STUDENTS' ACHIEVEMENTS

Students from the Artificial Intelligence and Data Science, Computer Science and Engineering, and Electronics and Communication Engineering departments emerged as winners in the prestigious AMD & Pervasive AI Developer Contest. Out of 144 ideas submitted globally, seven team concepts from Chettinad College of Engineering and Technology were selected as winners. We extend our heartfelt congratulations to all participants and offer special appreciation to the winning teams for their outstanding achievement.

**AMD & Pervasive AI Developer Contest Winners**

SHOOTING STARS FOUNDATION

Counselling Code **2630**

Revolutionizing Medical Image Analysis With AMD Generative AI GPU  
 III - ECE

Nishok Ananth E

Raghu B

Panthiruvallam K

Game Enhancing with AMD Ryzen AI  
 II - ECE

Arsath Ansari M

Sabareesan S

Pravin kumar S

Next-Gen Electric scooter  
 II - ECE

Saravankumar A

Navin S

Piragodesh T M

Out of 1455 ideas from all over the world, 516 were announced as winners to receive AMD Hardware.  
**7** ideas are from ChettinadTech.

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## PLACEMENTS



## Congratulations!

We congratulate our student who got selected as  
'Verification & Design Engineer' in

**SmartDV**



R. Sudhan  
ECE

Salary Package : 3.75 Lakhs per Annum



[www.chettinadtech.ac.in](http://www.chettinadtech.ac.in)

A blue banner celebrating student placements. It features the Chettinad logo, social media icons, and the website URL. The main text congratulates five students for being selected as 'Production Trainee' in WABCO. Below the text are five diamond-shaped portraits of the students, each with their name and department (ECE) listed underneath.

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*Congratulations!*

Our final year students placed as 'Production Trainee' in **WABCO**  
with the salary package of Rs.18,000/- per month

<b>ARAVINDH M</b> ECE	<b>LOGANATHAN P</b> ECE	<b>REVANTH S</b> ECE	<b>SATHIYAMOORTHY M</b> ECE	<b>VIGNESH J</b> ECE



**Chettinad**

College of Engineering & Technology

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