Chettinad College of Engineering & Technology
Approved by AICTE-New Delhi and Affiliated to Anna University-Chennai.

Newsletter Volume 3 Issue 1
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Editorial Board

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Mr.M.Prabhakaran, AP/ECE

Students

K.Sunil Prasad, III ECE

K.Nandhini, III ECE

R.Sudhan, II ECE

The Department of Electronics and Communication Engineering was established in the year 2007.The department has an intake of 60 students in B.E. course. The department possesses the most advanced equipment in its laboratories. It also provides opportunities to grow and excel in the technical world by conducting regular workshops and programs in various fields. The department is highly active in research work in the fields of broadband communications, VLSI Design, image processing etc. The students are provided adequate training in the field of signal processing, image processing and digital communication. Students are highly motivated to attend in-plant training in some of the most prestigious organizations during their time with the institution.

PROGRAM EDUCATIONAL OBJECTIVES (PEOs)

PEO1: To gain adequate knowledge to become good professional in electronic and communication engineering associated industries, higher education and research.

PEO2: To develop an attitude towards lifelong learning, applying and adapting new ideas and technologies as their field evolves.

PEO3: To prepare students to critically analyse existing literature in an area of specialization and ethically develop innovative and research-oriented methodologies to solve the problems identified

PROGRAM SPECIFIC OUTCOMES (PSOs)

PSO1: To analyze, design and develop solutions by applying foundational concepts of electronics and communication engineering.

PSO2: To apply design principles and best practices for developing quality products for scientific and business applications.

PSO3: To adapt to emerging information and communication technologies (ICT) to innovate ideas and solutions to existing/novel problems.

TAMILNADU STATE COUNCIL FOR SCIENCE AND TECHNOLOGY

Our ECE students Vijayalakshmi P, Valliammai M, Priyadharshini R, and Ayshwarya R have successfully secured a grant of ₹7,500 under the Student Project Scheme (SPS) funded by the Tamil Nadu State Council for Science and Technology (TNSCST) for their project titled “Dry Handwashing Using Fog to Save Water.”

VALUE ADDED COURSES

Value added courses aim to provide additional learner centric graded skill oriented technical training with the primary objective of improving the employability skills of the students and also to provide an opportunity to the students to develop interdisciplinary skills. Chettinad College of Engineering & Technology organized a five-day value-added course from 23.02.2022 to 09.03.2022 for all the II, III, IV year students.

PCB DESIGN AND IMPLEMENTATION OF ELECTRONIC CIRCUITS

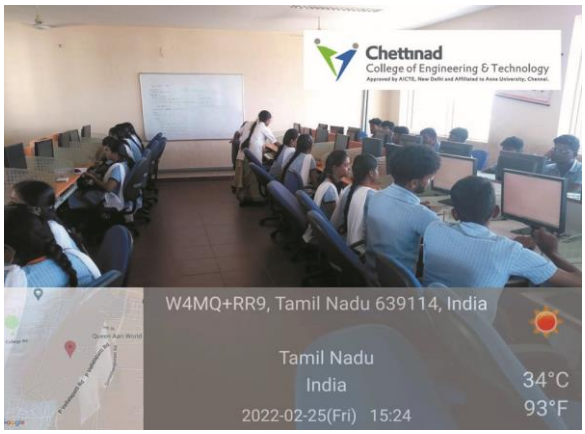
The Department of Electronics and Communication Engineering organized a value added course on **"PCB Design and implementation of Electronic Circuits using Multisim"** for II ECE students to give hands on training to design Printed Circuit Boards (PCB) and simulate electronic circuits using Multisim simulation software.

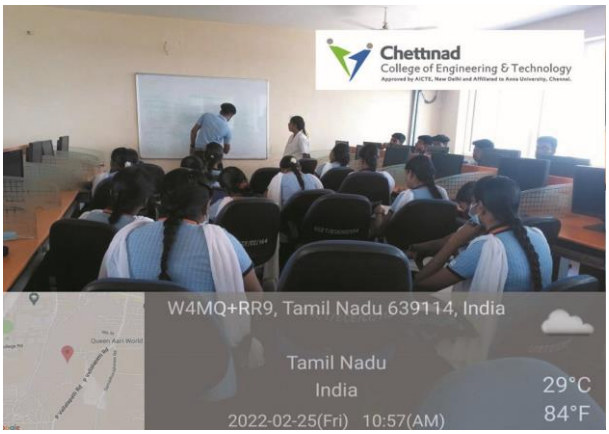
LABVIEW

The Department of ECE has organized a value added course on LABVIEW for III ECE students from 23.02.2022 to 02.03.2022. Mr.G.Nagaraj from Vi Solutions handled the sessions and gave insights to create a power train model for electric vehicle.

XILINX & MATLAB

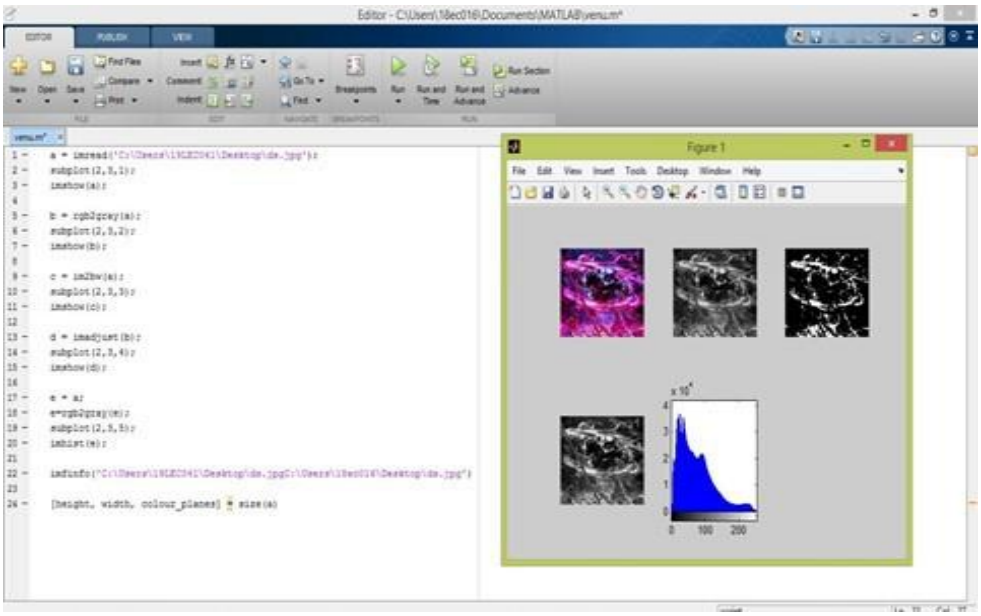
A Value-added course on “Xilinx and MATLAB” was arranged for the IV year students from 24.02.2022 to 01.03.2022. The students learnt the basics of Combinational circuits using logic gates, encoder and decoder design and basics of MATLAB, MATLAB compilation.



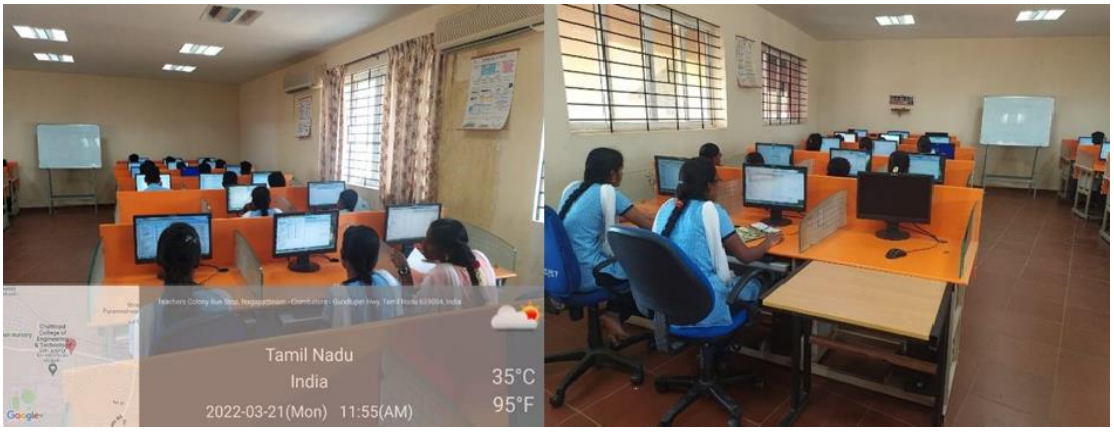


TECHNICAL ACTIVITY

A technical activity titled “Code with Matlab” was arranged for the IV year students on 19.03.2022. This activity helped the students to know more on block diagrams, modeling, prototype with specified blocks.



Generation of Signal using Simulink, a technical activity was conducted for the III year students on 21.03.2022. This activity helped the students to know about sine signal, cosine signal, unit step signal using simulink tool.



"Signal Sampling and Reconstruction" a technical activity was conducted for II ECE students on 22.03.2022. This activity enabled the students to explore how analog signals are converted to digital signals using digital transmissions systems.

INDUSTRIAL VISIT

On March 25th and 26th, 2022, ECE II, III, and final year students were on an industrial visit to Vikram Sarabhai Space Centre (VSSC) in Thiruvananthapuram and the Malanad Service Co- Operative Bank Ltd. (Masco) Tea Factory. Our students visited the Space Museum and learnt more about the ISRO programmes and the upcoming Gaganyan mission, as well as the functions of satellites and the various parts of satellite vehicles.

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